


[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

computer memory transaction "i/o" "exponential function"

- 2003

 Adv:
Sch:
Sch:

Scholar All articles - [Recent articles](#) Results 1 - 10 of about 373 for [computer memory transaction "i/o" "exponential function"](#)
All Results[D Heath](#)[M Cohen](#)[J Hennessy](#)[S Resnick](#)[G Samorodnitsky](#)
[\[PDF\] Characteristics of I/O Traffic in Personal Computer and Server Workloads](#) - all 8 versions »
WW Hsu, AJ Smith - *Computer*, 2002 - eecs.berkeley.edu... of the major development sites of a leading **computer** storage vendor ... be the size of the entire main **memory** in the ... 2, we present the fraction of I/O activity that ...Cited by 37 - [Related Articles](#) - [View as HTML](#) - [Web Search](#) - [Library Search](#)
[An accurate elementary mathematical library for the IEEE floating point standard](#) - all 3 versions »
S Gal - *ACM Transactions on Mathematical Software (TOMS)*, 1991 - portal.acm.org... 1991 ACM 0098-3500/91/0300-0026 \$01.50 *ACM Transactions on Mathematical ...* space allocatedto them in the **computer memory** and thus d cannot be too small. ...Cited by 43 - [Related Articles](#) - [Web Search](#)
[Computer technology and architecture: an evolving interaction](#) - all 6 versions »
JL Hennessy, NP Jouppi - *Computer*, 1991 - doi.ieeecomputersociety.org... pro- cessing systems, using the parallelism among **transactions**. ... cessors to share a bus and **memory sys-** tem ... of the rapid increase in **computer** performance seen ...Cited by 93 - [Related Articles](#) - [Web Search](#)
[The Hamming associative memory and its relation to the exponential capacity DAM](#) - all 2 versions »
MH Hassoun, PB Watta - *Neural Networks*, 1996, IEEE International Conference on, 1996 - ieexplore.ieee.org... Laboratory Department of Electrical & **Computer** Engineering Wayne ... of the Hamming associative **memory** and explored ... *Transactions on Neural Networks*, 2(2), 275-284. ...Cited by 9 - [Related Articles](#) - [Web Search](#)
[A recursive decomposition algorithm for network seismic reliability evaluation](#) - all 5 versions »
J Li, J He - *Earthquake Engineering & Structural Dynamics*, 2002 - doi.wiley.com... related to computation is an **exponential function** of the ... ie to decrease required storage **memory** so that ... the computation finished within the **computer** run time ...Cited by 20 - [Related Articles](#) - [Web Search](#)
[Heavy Tails and Long Range Dependence in on/off Processes and Associated Fluid Models](#) - all 4 versions »
D Heath, S Resnick, G Samorodnitsky - *Mathematics of Operations Research*, 1998 - JSTOR... 1. Since long range dependence and long **memory** are frequently ... *IEEE/ACM Transactions on Networking* 4 209-223 ... 38, Centre for Mathematics and **Computer** Science, CWI ...Cited by 134 - [Related Articles](#) - [Web Search](#)
[Self-organization for object extraction using a multilayer neural network and fuzziness measures](#) - all 3 versions »
A Ghosh, NR Pal, SK Pal - *Fuzzy Systems*, *IEEE Transactions on*, 1993 - ieexplore.ieee.orgPage 1 54 *IEEE TRANSACTIONS ON FUZZY SYSTEMS*, VOL ... is with the Division of **Computer** Science, University ... Hopfield's model of associative **memory**; • Kohonen's ...Cited by 45 - [Related Articles](#) - [Web Search](#)
[Opportunity cost algorithms for reduction of I/O and interprocess communication overhead in a ...](#) - all 12 versions »
A Keren, A Barak - *IEEE Transactions on Parallel and Distributed Systems*, 2003 - doi.ieeecs.org

Cited by 15 - Related Articles - Web Search

Cited by 9 - Related Articles - Web Search

Cited by 13 - Related Articles - Web Search

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

computer memory transaction "i/o" " Search

©2007 Google



[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

"i/o" "transaction" computer memory "exponential function"

- 2003

[Advanced Search](#)

Scholar All articles - [Recent articles](#) Results 11 - 20 of about 373 for "i/o" "transaction" computer memory "expo

All Results

[J Hennessy](#)

[D Heath](#)

[M Cohen](#)

[C Stapper](#)

[N Jouppi](#)

[Self-organization for object extraction using a multilayer neural network and fuzziness measures - all 3 versions »](#)

A Ghosh, NR Pal, SK Pal - Fuzzy Systems, IEEE Transactions on, 1993 - [ieeexplore.ieee.org](#)
Page 1 54 IEEE TRANSACTIONS ON FUZZY SYSTEMS, VOL ... is with the Division of Computer Science, University ... Hopfield's model of associative memory; • Kohonen's ...
Cited by 45 - [Related Articles](#) - [Web Search](#)

[Detection of fluctuating pulsed signals in the presence of noise](#)

P Swerling - Information Theory, IEEE Transactions on, 1957 - [ieeexplore.ieee.org](#)
... 176 IRE TRANSACTIONS ON INFORMATION THEORY ... $C(p) = \ln e^{-YW} & (10) \text{ Thus } I'(O) = -\$$
k ... Since digital computer programs exist for finding the eigenvalues of ...
Cited by 13 - [Related Articles](#) - [Web Search](#)

[Integrated circuit yield management and yield analysis: development and implementation - all 3 versions »](#)

CH Stapper, RJ Rosner - Semiconductor Manufacturing, IEEE Transactions on, 1995 - [ieeexplore.ieee.org](#)
... H U . 9 0 > 0 - 0 a IC I o so iso ... Page 2 (a) w 96 IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING ... within IBM, to the speedy development of computer programs for ...
Cited by 77 - [Related Articles](#) - [Web Search](#)

[Nonperiodic modes in two-dimensional \(2-D\) recursive digital filters under finite wordlength effects - all 2 versions »](#)

P Bauer, El Jury - Circuits and Systems, IEEE Transactions on, 1989 - [ieeexplore.ieee.org](#)
... merging and sorting in parallel models of computer," J. Camp ... $if(v/L) 0. (5) 1032$
IEEE TRANSACTIONS ON CIRCUITS ... with N processors on CRCW shared memory model ...
Cited by 4 - [Related Articles](#) - [Web Search](#)

[A 20 bit logarithmic number system processor - all 7 versions »](#)

FJ Taylor, R Gill, J Joseph, J Radke - IEEE Transactions on Computers, 1988 - [doi.ieeeecs.org](#)
... INTRODUCTION A number of meaningful general purpose computer and dedicated ... IEEE TRANSACTIONS ON COMPUTERS, VOL ... $Pi = eo \&oyi + eoxieoyi \text{ for } i=0 \text{ to } 18 \text{ Gi} = eoxieoyi \text{ ...}$
Cited by 63 - [Related Articles](#) - [Web Search](#)

[Index Scans Using a Finite LRU Buffer: A Validated I/O Model - all 3 versions »](#)

LF MACKERT, GUYM LOHMAN - ACM Transactions on Database Systems, 1989 - [portal.acm.org](#)
... Categories and Subject Descriptors: C.4 [Computer Systems Organization ... is compared with the actual I/O cost when ... scan on the R* ACM Transactions on Database ...
Cited by 47 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[\[PDF\] Modeling coarticulation in synthetic visual speech - all 8 versions »](#)

MM Cohen, DW Massaro - Models and Techniques in Computer Animation, 1993 - [dipaola.org](#)
... One general form for dominance is given by the negative exponential function, ... the images can also be imported to the computer using a video I/O board under ...
Cited by 328 - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[\[book\] Optimization of Mass Storage Hierarchies - all 2 versions »](#)

... Dept. of Electrical Engineering and Computer ... - 1994 - [eecs.umich.edu](#)
... larger tertiary storage, and the semiconductor memory becomes merely a higher-level cache. The enormity of I/O space is changing the way computer systems are ...
Cited by 3 - [Related Articles](#) - [View as HTML](#) - [Web Search](#) - [Library Search](#)

[Efficient sequence detection for intersymbol interference channels with run-length constraints](#)

[- all 4 versions »](#)

J Moon, LR Carley - Communications, IEEE Transactions on, 1994 - [ieeexplore.ieee.org](#)

... with the Department of Electrical and Computer Engineer- ing ... interval, then Page

2 IEEE **TRANSACTIONS ON COMMUNICATIONS**. ... made without looking back through i=O i=r ...

[Cited by 23](#) - [Related Articles](#) - [Web Search](#)

[Sampling rate conversion - all 3 versions »](#)

Al Russell, PE Beckmann - US Patent 6,665,694, 2003 - [Google Patents](#)

... Conversion Between Arbitrary Sampling Frequencies IEE **Transactions** on Acoustics ...

o UJ ... Processor 56 executes **computer-executable** instructions 61 out of **memory** ...

[Cited by 2](#) - [Related Articles](#) - [Web Search](#)



Result Page: [Previous](#) [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [11](#) [Next](#)

"i/o" "transaction" computer memory

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2007 Google


[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide

+transaction +"i/o" exponential function



THE ACM DIGITAL LIBRARY


[Feedback](#) [Report a problem](#) [Satisfaction survey](#)

Published before September 2003

Terms used: [transaction](#) [i/o](#) [exponential function](#)

Found 9,637 of 148,072

Sort results by **relevance**Display results **expanded form**
[Save results to a Binder](#)
[Search Tips](#)
☐ [Open results in a new window](#)
Try an [Advanced Search](#)Try this search in [The ACM Guide](#)

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

Best 200 shown

Relevance scale ☐ ☐ ☐ ☐ ☐

1 [Uniform Asymptotic Expansions for Exponential Integrals \$E_n\(x\)\$ and Bickley](#)

[Functions Kin \(x\)](#)

D. E. Amos

December 1983

ACM Transactions on Mathematical Software (TOMS), Volume 9 Issue 4

Publisher: ACM Press

Full text available: [pdf\(554.47 KB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

2 [ATPG tools for delay faults at the functional level](#)



M. Michael, S. Tragoudas

January 2002

ACM Transactions on Design Automation of Electronic Systems (TODAES),

Volume 7 Issue 1

Publisher: ACM Press

Full text available: [pdf\(324.45 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present an ATPG tool for functional delay faults which applies to the single-input transition (SIT) and the multi-input transition (MIT) fault models, and is based on Reduced Ordered Binary Decision Diagrams (ROBDDs). We are able, for the first time, to identify all faults that do not have any SIT tests, and generate all SIT tests for nonredundant faults in combinational circuits. We also provide methodologies for efficient generation of MIT tests. Our experimental results on the ISCAS'85 ben ...

Keywords: Automatic test pattern generation, Binary Decision Diagrams, Boolean Satisfiability, delay testing, functional-level testing, path delay fault simulation (coverage), path delay fault testing, testing digital circuits

3 [Table-driven implementation of the exponential function in IEEE floating-point arithmetic](#)



Ping-Tak Peter Tang

June 1989

ACM Transactions on Mathematical Software (TOMS), Volume 15 Issue 2

Publisher: ACM Press

Full text available: [pdf\(803.40 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Algorithms and implementation details for the exponential function in both single- and double-precision of IEEE 754 arithmetic are presented here. With a table of moderate size, the implementations need only working-precision arithmetic and are provably accurate to within 0.54 ulp as long as the final result does not underflow. When the final result suffers gradual underflow, the error is still no worse than 0.77 ulp.

Keywords: IEEE arithmetic, exponential function, implementation

4 [ACE: an automatic complexity evaluator](#)

Daniel Le Métayer



April 1988 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,
Volume 10 Issue 2
Publisher: ACM Press
Full text available: pdf(1.27 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

There has been a great deal of research done on the evaluation of the complexity of particular algorithms; little effort, however, has been devoted to the mechanization of this evaluation. The ACE (Automatic Complexity Evaluator) system is able to analyze reasonably large programs, like sorting programs, in a fully mechanical way. A time-complexity function is derived from the initial functional program. This function is transformed into its nonrecursive equivalent according to MacCarthy's ...



5 [A methodology for the design of distributed information systems](#)
Giacomo Bucci, Donald N. Streeter
April 1979 **Communications of the ACM**, Volume 22 Issue 4

Publisher: ACM Press
Full text available: pdf(1.16 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

A macro model of a distributed information system is presented. The model describes the major costs of using an information system from the perspective of the end-user. The model is intended to provide guidance to the system designer by making evident the effect of various design and operating parameters on overall cost per transaction. The technique is illustrated by application to the design of an interactive transaction processing system.

Keywords: cost minimization, distributed database, distributed processing, economic modeling, interactive computing, system design, transaction processing



6 [Bounded index exponential hashing](#)
David B. Lomet
March 1983 **ACM Transactions on Database Systems (TODS)**, Volume 8 Issue 1

Publisher: ACM Press
Full text available: pdf(1.97 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Bounded index exponential hashing, a new form of extendible hashing, is described. It has the important advantages over most of the other extendible hashing variants of both (i) providing random access to any record of a file in close to one disk access and (ii) having performance which does not vary with file size. It is straightforward to implement and demands only a fixed and specifiable amount of main storage to achieve this performance. Its underlying physical disk storage is readily m ...

Keywords: extendible hashing, tree index methods



7 [Use of performance analysis statistics in computer system simulation](#)
P. R. Katonak
January 1971 **Proceedings of the 5th conference on Winter simulation WSC '71**

Publisher: ACM Press
Full text available: pdf(794.16 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A general purpose job stream simulation model has been developed for routine use in evaluating hardware configurations and performance tuning a large scale multiprocessor computing system operating in a multiprogramming mode. The model is designed to simulate a variable workload of jobs statistically generated from attributes of the actual workload for any time period. Major functional modules include job-step generation, job classing, scheduling and step initiation, core allocation, cpu op ...



8 [Probabilistic query models for transaction data](#)
Dmitry Pavlov, Padhraic Smyth
August 2001 **Proceedings of the seventh ACM SIGKDD international conference on Knowledge discovery and data mining KDD '01**

Publisher: ACM Press
Full text available: pdf(958.33 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We investigate the application of Bayesian networks, Markov random fields, and mixture models to the problem of query answering for transaction data sets. We formulate two versions of the querying problem: the query selectivity estimation (i.e., finding exact

counts for tuples in a data set) and the query generalization problem (i.e., computing the probability that a tuple will occur in new data). We show that frequent itemsets are useful for reducing the original data to a compressed representa ...

9 Procedure cloning: a transformation for improved system-level functional partitioning



Frank Vahid

January 1999

ACM Transactions on Design Automation of Electronic Systems (TODAES),

Volume 4 Issue 1

Publisher: ACM Press

Full text available: pdf(227.98 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Functional partitioning assigns the functions of a system's program-like specification among system components, such as standard-software and custom-hardware processors. We introduce a new transformation, called procedure cloning, that significantly improves functional partitioning results. The transformation creates a clone of a procedure for sole use by a particular procedure caller, so the clone can be assigned to the caller's processor, which in turn improves performance through reduced ...

Keywords: behavioral synthesis, embedded systems, functional partitioning, hardware/software codesign, replication, system-level design, system-on-a-chip, transformations

10 Disk file allocation based on the buddy system



Philip D. L. Koch

October 1987

ACM Transactions on Computer Systems (TOCS), Volume 5 Issue 4

Publisher: ACM Press

Full text available: pdf(1.57 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

The buddy system is known for its speed and simplicity. However, high internal and external fragmentation have made it unattractive for use in operating system file layout. A variant of the binary buddy system that reduces fragmentation is described. Files are allocated on up to t extents, and inoptimally allocated files are periodically reallocated. The Dartmouth Time-Sharing System (DTSS) uses this method. Several installations of DTSS, representing different classes of w ...

11 Exploiting process lifetime distributions for dynamic load balancing



Mor Harchol-Balter, Allen B. Downey

August 1997

ACM Transactions on Computer Systems (TOCS), Volume 15 Issue 3

Publisher: ACM Press

Full text available: pdf(557.04 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We consider policies for CPU load balancing in networks of workstations. We address the question of whether preemptive migration (migrating active processes) is necessary, or whether remote execution (migrating processes only at the time of birth) is sufficient for load balancing. We show that resolving this issue is strongly tied to understanding the process lifetime distribution. Our measurements indicate that the distribution of lifetimes for a UNIX process is Pareto (heavy-tailed), with ...

Keywords: Pareto distribution, heavy-tailed, load balancing, load sharing, migration, network of workstations, remote execution, trace-driven simulation, workload modeling

12 Analysis of locking policies in database management systems



D. Potier, Ph. Leblanc

October 1980

Communications of the ACM, Volume 23 Issue 10

Publisher: ACM Press

Full text available: pdf(906.50 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Consistency control has to be enforced in database management systems (DBMS) where several transactions may concurrently access the database. This control is usually achieved by dividing the database into locking units or granules, and by specifying a locking policy which ensures integrity of the information. However, a drawback of integrity enforcement through locking policies is the degradation of the global system performance. This is mainly due to the restriction imposed by the locking ...

Keywords: concurrency, consistency, database management, performance modeling,

queueing models, queueing networks

13 [A note on the quality of random variates generated by the ratio of uniforms method](#)



Wolfgang Hörmann

January 1994

ACM Transactions on Modeling and Computer Simulation (TOMACS),

Volume 4 Issue 1

Publisher: ACM Press

Full text available: [pdf\(611.51 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

The one-dimensional distribution of pseudorandom numbers generated by the ratio of uniforms method using linear congruential generators (LCGs) as the source of uniform random number is investigated in this note. Due to the two-dimensional lattice structure of LCGs there is always a comparable large gap without a point in the one-dimensional distribution of any ratio of uniforms method. Lower bounds for these probabilities only depending on the modulus and the Beyer quotient of the LCG are p ...

Keywords: discrepancy, linear congruential generator, ratio of uniforms method

14 [Variance reduction applied to product form multiclass queueing networks](#)



Bruno Tuffin

October 1997

ACM Transactions on Modeling and Computer Simulation (TOMACS),

Volume 7 Issue 4

Publisher: ACM Press

Full text available: [pdf\(183.06 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Performance of product-form multiclass queueing networks can be determined from normalization constants. For large models, the evaluation of these performance metrics is not possible because of the required amount of computer resources (either by using normalization constants or by using MVA approaches). Such large models can be evaluated with Monte Carlo summation and integration methods. This article proposes two cluster sampling Monte Carlo techniques to deal with such models. First, for ...

Keywords: Monte Carlo, antithetic variates, low discrepancy sequences, product-form networks, variance reduction

15 [Operating system principles](#)

Per Brinch Hansen

January 1973

Book

Publisher: Prentice-Hall, Inc.

Full text available: [pdf\(16.81 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

From the Preface

MAIN GOAL

This book tries to give students of computer science and professional programmers a general understanding of *operating systems*--the programs that enable people to share computers efficiently.

To make the sharing of a computer tolerable, an operating system must enforce certain rules of behavior on all its users. One would therefore expect the designers of operating systems to do their utmost to make them as s ...

16 [Smalltalk-80: the language and its implementation](#)

Adele Goldberg, David Robson

January 1983

Book

Publisher: Addison-Wesley Longman Publishing Co., Inc.

Full text available: [pdf\(33.56 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [cited by](#), [index terms](#), [review](#)

From the Preface (See Front Matter for full Preface)

Advances in the design and production of computer hardware have brought many more people into direct contact with computers. Similar advances in the design and production


of computer software are required in order that this increased contact be as rewarding as possible. The Smalltalk-80 system is a result of a decade of research into creating computer software that is appropriate for producing highly functional and interactive ...

17 Imbedding GPSS in a general purpose programming language

Jerrold Rubin

January 1981 **Proceedings of the 13th conference on Winter simulation - Volume 1 WSC '81**

Publisher: IEEE Press

Full text available:  [pdf\(432.26 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


GPSS has proven to be an excellent simulation language, but was not designed to perform the logical and computational tasks of a programming language. Strategies for improvement can take one of two paths: building adequate analytic constructs into the existing GPSS language, or building GPSS-like constructs into an existing general purpose programming language. The second choice can be made easier by use of a language-building utility program to translate simulation constructs into the sele ...

18 Performance analysis of checkpointing strategies

Asser N. Tantawi, Manfred Ruschitzka

May 1984 **ACM Transactions on Computer Systems (TOCS)**, Volume 2 Issue 2

Publisher: ACM Press

Full text available:  [pdf\(1.15 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)


Keywords: database recovery, equicost checkpointing strategy, equidistant checkpointing strategy, error recovery, performance modeling and optimization, rollback recovery, system availability

19 Simulation and mathematical modeling of an on-line accounting system

Jack E. Shemer

December 1970 **Proceedings of the fourth annual conference on Applications of simulation**

Publisher: Winter Simulation Conference

Full text available:  [pdf\(614.58 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#)


Two approaches are presented for analyzing and predicting performance of an on-line business data accounting system. The computer system's responsiveness to typical (high priority) file updating demands is examined by two methods of a priori study: simulation and mathematical modeling. A discrete event simulation model, written in FORTRAN, is developed to comprehensively describe system performance. This simulation model is based upon the isolation and sequential ordering of logical " ...

20 Cryptography and data security

Dorothy Elizabeth Robling Denning

January 1982 Book

Publisher: Addison-Wesley Longman Publishing Co., Inc.





Full text available:  [pdf\(19.47 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

From the Preface (See Front Matter for full Preface)

Electronic computers have evolved from exiguous experimental enterprises in the 1940s to prolific practical data processing systems in the 1980s. As we have come to rely on these systems to process and store data, we have also come to wonder about their ability to protect valuable data.

Data security is the science and study of methods of protecting data in computer and communication systems from unauthorized disclosure ...

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Purchase History](#) |

Welcome United States Patent and Trademark Office

☐ Search Results
[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((('i/o' and transaction and exponential and function)<in>metadata)) <and> (pyr >..."

Your search matched 2 of 1706580 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.



» Search Options

[View Session History](#)[New Search](#)

» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

Modify Search

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract[IEEE/IET](#)[Books](#)[Educational Courses](#)[A](#)

IEEE/IET journals, transactions, letters, magazines, conference proceedings, and

[Select All](#) [Deselect All](#)

- ☐ 1. Optimal design for a ball grid array wire bonding process using a neuro-
Chao-Ton Su; Tai-Lin Chiang;
Electronics Packaging Manufacturing, IEEE Transactions on [see also Comp
Manufacturing Technology, Part C: Manufacturing, IEEE Transactions on]
Volume 25, Issue 1, Jan. 2002 Page(s):13 - 18
Digital Object Identifier 10.1109/TEPM.2002.1000478
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(264 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 2. Analysis and design of I/O based variable structure adaptive control
Liu Hsu; de Araujo, A.D.; Costa, R.R.;
Automatic Control, IEEE Transactions on
Volume 39, Issue 1, Jan. 1994 Page(s):4 - 21
Digital Object Identifier 10.1109/9.273335
[AbstractPlus](#) | Full Text: [PDF\(1512 KB\)](#) IEEE JNL
[Rights and Permissions](#)

 Indexed by
 Inspec®
[Help](#) [Contact Us](#)

© Copyright 20